



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

HWANG et al.

Application No.: 10/749,533

Filed: December 30, 2003

For: METHOD FOR FABRICATING
SEMICONDUCTOR DEVICE

Customer No.: 20350

Confirmation No. 8876

Examiner:

Technology Center/Art Unit: 2812

REQUEST FOR CORRECTED FILING
RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the Patent Office records for the above-identified application be corrected with respect to the foreign priority information. Specifically, the filing date of the foreign priority application should be listed as March 14, 2003, as noted on the first page of the previously submitted priority document. A supplemental Application Data Sheet is included herewith to facilitate this change. In the event a fee is required for this correction, the Commissioner is authorized to charge that fee or any additional fee to the undersigned's Deposit Account No. 20-1430.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 415-576-0200.

Respectfully submitted,

Babak S. Sani
Reg. No. 37,495

TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, Eighth Floor
San Francisco, California 94111-3834
Tel: 415-576-0200 / Fax: 415-576-0300
BSS:deb
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